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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

PATENT APPLICATION EXAMINING OPERATIONS

LDS
6-18-02
entered

Applicant: Brian DONOVAN

Group Art Unit: 2783

Serial N^o: 09/410,202

Examiner: D. Eng

Filed: September 30, 1999

Title: ZERO OVERHEAD COMPUTER INTERRUPTS WITH TASK
SWITCHING

AMENDMENT

1600 ODS Tower
601 SW. Second Avenue
Portland, Oregon 97204-3157

May 10, 2002

Assistant Commissioner of Patents
Washington, D.C. 20231

Dear Sir:

Responsive to the office action mailed November 2, 2001, please amend the application as follows.

IN THE CLAIMS

1 / / / / Rewrite claims 2, 4, 6, 7, 8 and 9 as follows.

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MAY 2 9 2002

OFFICE OF PETITIONS

2. A method for ordering the performance of tasks in a computer system, said computer system having input sources that issue interrupt signals for requesting the performance of a task, said method comprising:

(a) providing an integrated circuit having circuit components for

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